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CS401-Assembly Language Programming Update MCQ'S Mid Term



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Question No:1

(Marks:1)

Vu-Topper RM

Which of the following instruction is equivalent to multiplying the value of AX by 8?

- A. SHL AX, 3**
- B. MUL AX, 3
- C. SHL AX, 8
- D. SAL AX, 8

Question No:2

(Marks:1)

Vu-Topper RM

In _____, a zero is inserted from the right, and every bit moves one position to its left. The most significant bit also drops into the carry flag.

- A. Shift Arithmetic Right (SAR)
- B. Shift Arithmetic Left (SAL)**
- C. Shift logical Right (SHR)
- D. Rotate Right (ROR)

Question No:3

(Marks:1)

Vu-Topper RM

What is the purpose of MOV AX, 0xFFFF?

- A. Store 0xFFFF into AX**
- B. Add 0xFFFF into AX
- C. Move AX into 0xFFFF
- D. Copy 0xFFFE into AX

Question No:4

(Marks:1)

Vu-Topper RM

_____ decrements SP (the stack pointer) by two, and then transfers a word from the source operand to the top of the stack.

- A. RET
- B. POP
- C. CALL

D. PUSH **Page 63**

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Question No:5

(Marks:1)

Vu-Topper RM

In assembly language, _____ is used to load a character in AL.

- A. Lods b
- B. Lod al
- C. lod b b
- D. Lod b s**

Question No:6

(Marks:1)

Vu-Topper RM

A symbol associated to a point in the program is called a _____.

- A. Label**
- B. Subroutines
- C. JUMP
- D. Stack

Question No:7

(Marks:1)

Vu-Topper RM

In _____ operation, the carry flag is inserted from the right causing every bit to move one location to its left. The dropped bit from the left goes into the carry flag.

- A. Rotate through Carry Left (RCL)
- B. Rotate Right (ROR)
- C. Rotate through Carry Right (RCR)**
- D. Rotate Left (ROL)

Question No:8

(Marks:1)

Vu-Topper RM

Choose the correct default segment associations.

- A. IP=>BP, SP=>SS and BX=>DS
- B. CX=>CS, DI=>DS and SI=>SS
- C. IP=>CS, SP=>SS and BX=>DS
- D. IP=>CS, SP=>DS and SI=>SS**

Question No:9

(Marks:1)

Vu-Topper RM

The ASCII code for digit 8 is _____.

- A. 0x80

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B. 0x18

C. 0x38

D. 0x28

Question No:10

(Marks:1)

Vu-Topper RM

The main difference between standard and extended ASCII tables is the number of defined _____.

A. serial ports

B. rows and columns on VGA

C. I/O ports

D. symbols

Question No:11

(Marks:1)

Vu-Topper RM

After the execution of STOSB and STOSW, the CX register will be _____ by _____.

A. decremented, 1

B. incremented, 2

C. incremented, 1

D. decremented, 2

Question No:12

(Marks:1)

Vu-Topper RM

In iAPX88, when an element is popped from the stack, SP is _____ by 2.

A. incremented

B. divided

C. multiplied

D. decremented

Question No:13

(Marks:1)

Vu-Topper RM

The symbols in extended ASCII table by IBM fits in one _____.

A. byte

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- B. word
- C. doubleword
- D. bit

Question No:14 (Marks:1) **Vu-Topper RM**
Which of the following is a non-destructive AND operation?

- A. CMP
- B. ADC
- C. NAND
- D. TEST**

Question No:15 (Marks:1) **Vu-Topper RM**
ASCII stands for_____.

- A. American Standard Code for Information Interaction
- B. American Standard Code for Information Interchange**
- C. American Scientific Communication for Integer Interaction
- D. American Standard Communication for Integer Interchange

Question No:16 (Marks:1) **Vu-Topper RM**
Which of the following is the correct instruction for NOT operation?

- A. not byte [var], ax
- B. not bx**
- C. not word [var], ax
- D. not ax, bx

Question No:17 (Marks:1) **Vu-Topper RM**
The _____ flag sets if the last mathematical or logical instruction has produced a zero in its destination.

- A. trap
- B. sign
- C. zero**
- D. Interrupt

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Question No:18

(Marks:1)

Vu-Topper RM

Which type of jump(s) cover the whole memory?

Short

Near

Far

A. I and II

B. II

C. III

D. I

Question No:19

(Marks:1)

Vu-Topper RM

Shift logical Left (SHL) moves all the bits one position to the _____ and inserts a zero from the _____.

A. left, left

B. left, right

C. Right, left

D. right, right

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Question No:20

(Marks:1)

Vu-Topper RM

Which of these hexadecimal numbers is equal to -40 in 2's complement?

A. FFDD

B. FFFF

C. FFEE

D. FFD8

Question No:21

(Marks:1)

Vu-Topper RM

Physical memory address in Intel 8088 processor is determined by the _____.

A. Data Segment register

B. Stack Segment register

C. Code Segment register

D. Segment register and offset

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Question No:22

(Marks:1)

Vu-Topper RM

Which of following assembly code best matches with the instruction A17528?

- A. mov ax 2875
- B. mov ax, [7528]**
- C. mov ax, 7528
- D. mov ax, [2875]

Question No:23

(Marks:1)

Vu-Topper RM

In _____ operation, a carry flag is inserted from the left moving every bit one position to its right. The right most bit is dropped into the carry flag.

- A. Rotate Left (ROL)
- B. Rotate Right (ROR)
- C. Rotate through Carry Left (RCL)
- D. Rotate through Carry Right (RCR)**

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Question No:24

(Marks:1)

Vu-Topper RM

Which of following conditional jump is not dependent on any flag?

- A. JNG
- B. JCXZ**
- C. JP
- D. JPE

Question No:25

(Marks:1)

Vu-Topper RM

Verification of data integrity between sender and receiver is performed by the _____ flag.

- A. zero
- B. parity**
- C. sign
- D. auxiliary carry

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Question No:26

(Marks:1)

Vu-Topper RM

Which of the following instructions causes the size mismatch error?

- A. mov al, ax
- B. mov byte[num1], al
- C. mov word[num1], bx
- D. mov ax, bx**

Question No:27

(Marks:1)

Vu-Topper RM

There are _____ types of address wraparounds.

- A. 4
- B. 2**
- C. 1
- D. 3

Question No:28

(Marks:1)

Vu-Topper RM

What would be the output of MOV AX, BL?

- A. The contents of the BL register will be moved to the AX register, and the BL will be set as 0
- B. Assembler will declare it as an illegal expression
- C. The contents of the AX register will be moved to the BL register
- D. The contents of the BL register will be moved to the AX register**

Question No:29

(Marks:1)

Vu-Topper RM

In Far jump, the segment address is stored in _____.

- A. CS**
- B. ES
- C. DS
- D. SS

Question No:30

(Marks:1)

Vu-Topper RM

As a result of base+offset addressing, the address that is generated is called a (an) _____ address.

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- A. Not
- B. Elective
- C. Additive**
- D. Effective

Question No:31 (Marks:1) **Vu-Topper RM**
MOV AX, 0X4C00INT 0X21 Which of the following best describes the purpose of the given instructions?

- A. Variable definition
- B. Move number into ax
- C. Terminate a program**
- D. Move ax into 0X4C00

Question No:32 (Marks:1) **Vu-Topper RM**
The instruction for permanent diversion in Intel 8088 is _____.

- A. Rot
- B. Mov
- C. Call
- D. Jmp**

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Question No:33 (Marks:1) **Vu-Topper RM**
The top of stack is contained in _____ register.

- A. Bx
- B. SP**
- C. Ax
- D. Bp

Page 60

Question No:34 (Marks:1) **Vu-Topper RM**
There are _____ registers in Intel 8088 that can hold the address of data.

- A. 4**
- B. 8

Page 22

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- C. 10
- D. 12

Question No:35 (Marks:1) **Vu-Topper RM**

Which of the following is the number of operands for ADC (Add with Carry) instruction?

- A. 1
- B. 2**
- C. 3
- D. 4

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Question No:36 (Marks:1) **Vu-Topper RM**

The jump is taken if the last arithmetic operation has changed the sign unexpectedly.

- A. JZN
- B. JZ
- C. JNO
- D. JO**

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Question No:37 (Marks:1) **Vu-Topper RM**

Which of the following is shifted right in the 4-bit multiplication algorithm?

- A. Multiplicand
- B. Multiplier**
- C. RCL
- D. OLC

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Question No:38 (Marks:1) **Vu-Topper RM**

Extended Shifting Algorithm consists of:

- A. 4 instructions
- B. 2 instructions**
- C. 1 instruction
- D. 5

instructions

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Question No:39

(Marks:1)

Vu-Topper RM

All the addressing mechanisms in iAPX88 return a number called the _____ address.

- A. Faulty
- B. Direct
- C. Indirect

D. Effective

Page 25

Question No:40

(Marks:1)

Vu-Topper RM

Call instruction takes _____ argument(s).

- A. 0
- B. 1
- C. 2
- D. 3

Page 64

Question No:41

(Marks:1)

Vu-Topper RM

With the execution of CALL instruction, the value of _____ is decremented by 2.

- A. SP
- B. BX
- C. CX
- D. DX

Page 60

Question No:42

(Marks:1)

Vu-Topper RM

Which bit of the attribute byte represents the red component of foreground color?

- A. 2
- B. 4
- C. 6
- D. 8

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Question No:43

(Marks:1)

Vu-Topper RM

In MUL instruction, if the source operand is a byte, then the result is returned in _____ and _____.

A. AH,AL **Page 79**

B. AX,BL

C. HL,AX

D. AH,LX

Question No:44

(Marks:1)

Vu-Topper RM

_____ can be used to check whether particular bits of a number are set or not.

A. OR

B. AND **Page 52**

C. NOT

D. XOR

Question No:45

(Marks:1)

Vu-Topper RM

RET Instruction takes _____ argument(s).

A. 0 **Page 64**

B. 1

C. 2

D. 3

Question No:46

(Marks:1)

Vu-Topper RM

When the RET instruction is executed, it recovers the value of the _____ from the stack.

A. Current instruction

B. Accumulator register

C. Previous instruction

D. Instruction pointer **Page 68**

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Question No:47

(Marks:1)

Vu-Topper RM

Stack is a data structure that behaves in a First In Last _____ manner.

- A. In
- B. Add
- C. Out**
- D. Posh

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Question No:48

(Marks:1)

Vu-Topper RM

We can convert a digit into its ASCII representation by adding _____ to it.

- A. 0X05
- B. 0X10
- C. 0X20
- D. 0x30**

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Question No:49

(Marks:1)

Vu-Topper RM

_____ function decrements SP (the stack pointer) by two, and then transfers a word from the source operand to the top of stack now pointed to by SP.

- A. RET
- B. POP
- C. CALL
- D. PUSH**

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Question No:50

(Marks:1)

Vu-Topper RM

In 8088, there is a _____ stack.

- A. Stagnant
- B. Incrementing

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C. Decrementing

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D. Semi-automatic

Question No:51

(Marks:1)

Vu-Topper RM

There are _____ byte(s) for each character on the screen.

A. 2 Page 87

B. 4

C. 1

D. 5

Question No:52

(Marks:1)

Vu-Topper RM

In case of DIV BX instruction, the quotient is stored in _____ register.

A. AX Page 85

B. DX

C. AL

D. BL

Question No:53

(Marks:1)

Vu-Topper RM

Call Instruction takes a _____ as an argument.

A. Label Page 64

B. source register

C. memory address

D. destination register

Question No:54

(Marks:1)

Vu-Topper RM

A decrementing stack moves from _____ to _____ addresses as elements are added in it.

A. lower, higher

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B. higher, lower Page 60

C. positive, negative

D. negative, positive

Question No:55

(Marks:1)

Vu-Topper RM

Which of the following operations is used to set any specific bit in a binary number?

A. OR Page 59

B. AND

C. NOT

D. XOR

Question No:56

(Marks:1)

Vu-Topper RM

Which of the following operations is used to clear any specific bit in a binary number?

A. AND Page 59

B. XOR

C. NOT

D. NAND

Question No:57

(Marks:1)

Vu-Topper RM

Which of the following string instructions is generally used in a loop instead of REP prefix?

A. SCAS

B. CMPS

C. STOS

D. LODS Page 84

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Question No:58

(Marks:1)

Vu-Topper RM

What the following piece of code does?

Shl word [multiplicand], 1

Rcl word [multiplicand+2], 1

A. Extended Shift right

B. Extended Shift left **Page 58**

C. Rotate multiplicand and multiplicand+2

D. Multiply multiplicand and multiplicand+2

Question No:59

(Marks:1)

Vu-Topper RM

What does the PUSH operation do?

A. It increments the stack pointer

B. It increments the stack segment

C. It copies the operand on the stack **Page 60**

D. It deletes the operand from the stack

Question No:60

(Marks:1)

Vu-Topper RM

Which of the following is an incorrect XOR operation?

A. XOR AX

B. XOR BX,AX **Page 59**

C. XOR AX, BX

D. XOR BYTE [VAR], 5

Question No:61

(Marks:1)

Vu-Topper RM

Stack Pointer is _____, when the ret instruction is executed.

A. divided by 2

B. multiplied by 2

C. decremented by 2

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D. incremented by 2

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Question No:62

(Marks:1)

Vu-Topper RM

The operand of _____ is called the source operand because the data moves to the stack from the operand.

- A. RET
- B. CALL
- C. POP

D. PUSH

Page 71

Question No:63

(Marks:1)

Vu-Topper RM

The memory on video controller is accessible to the processor by the _____ bus.

- A. Cache
- B. Control

C. System

Page 72

D. Virtual memory

Question No:64

(Marks:1)

Vu-Topper RM

The operand of POP is called the _____ because the data is moving from the stack to the operand.

- A. Source
- B. Increment
- C. Decrement

D. Destination

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Question No:65

(Marks:1)

Vu-Topper RM

In a video memory, each screen location corresponds to _____ byte(s).

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A. One

B. Two

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C. Four

D. Eight

Question No:66

(Marks:1)

Vu-Topper RM

The unit of stack operations in iAPX88 is a _____.

A. Bit

B. Byte

C. Word

Page 68

D. Double word

Question No:67

(Marks:1)

Vu-Topper RM

In XOR operation, the output is 1 if both inputs are _____.

A. Same

B. True

C. False

D. Different

Page 59

Question No:68

(Marks:1)

Vu-Topper RM

Which of the following instructions allows code reusability in 8088?

A. RET

B. MOV

C. ADD

D. CALL

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Question No:69

(Marks:1)

Vu-Topper RM

Which bit of the attribute byte represents the green component of foreground color?

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A. 0

B. 1 Page 73

C. 2

D. 3

Question No:70

(Marks:1)

Vu-Topper RM

Which of the following is also called intra-segment call?

A. Far call

B. Near call Page 72

C. Short call

D. Long call

Question No:71

(Marks:1)

Vu-Topper RM

The stack of iAPX88 works on _____ sized elements.

A. Byte

B. Nibble

C. Word Page 60

D. Paragraph

Question No:72

(Marks:1)

Vu-Topper RM

In 8051, there is an _____ stack.

A. Stagnant

B. Decrementing

C. Semi-automatic

D. Incrementing Page 68

Question No:73

(Marks:1)

Vu-Topper RM

The maximum parameters a subroutine can receive are _____
when all the general purpose registers are used.

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A. 1

B. 3

C. 5

D. 7 **Page 64**

Question No:74

(Marks:1)

Vu-Topper RM

How many characters are defined by the standard ASCII?

A. 32

B. 64

C. 132

D. 128 **Page 71**

Question No:75

(Marks:1)

Vu-Topper RM

Which of the following statement is used to clear the value of AX register?

A. NOT AX

B. XOR AX,0

C. XOR AX, AX

D. AND AX, AX

Question No:76

(Marks:1)

Vu-Topper RM

MUL instruction performs an unsigned multiplication of the source operand and the _____.

A. Base

B. Carry

C. Accumulator **Page 79**

D. Word at ES:DI

Question No:77

(Marks:1)

Vu-Topper RM

The clear screen operation initializes the whole block of video memory to _____.

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- A. 0x0417
- B. 0x0714
- C. 0x0721**
- D. 0x0174

Question No:78 (Marks:1) **Vu-Topper RM**

Which of the following elements will come out first using the POP instruction if 6, 3, 5, 8, 4, 7 and 9 were pushed on the stack?

- A. 9**
- B. 8
- C. 7
- D. 6

Question No:79 (Marks:1) **Vu-Topper RM**

To convert the case of a character, we add or subtract _____ from its ASCII code.

- A. 0X20**
- B. 0X10
- C. 0X41
- D. 0X30

Question No:80 (Marks:1) **Vu-Topper RM**

Scrolling is the process of moving one or more lines towards the top or bottom of the screen, and the new line that appears on the top or bottom is _____.

- A. Cleared**
- B. Add
- C. Different
- D. None of these

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Question No:81 (Marks:1)

Vu-Topper RM

How many are the functions of a register?

A. Four

B. Five

C. One

D. Two

Question No:82 (Marks:1)

Vu-Topper RM

Which of the following is not a valid instruction in assembly language?

A. MOV AX, 55

B. MOV AX, BX

C. MOV CS, 0xb800

D. MOV BX, AX

Question No:83 (Marks:1)

Vu-Topper RM

By default, BP is associated with _____.

A. CS

B. SS

C. IP

D. SP

Question No:84 (Marks:1)

Vu-Topper RM

Execution of CALL instruction affects _____ number of bits in Flags register.

A. 1

B. 2

C. 3

D. 4

Question No:85 (Marks:1)

Vu-Topper RM

If AND operation is performed between AX and BX, then how many AND operations will be performed in total?

A. 2

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- B. 8
- C. 16**
- D. 32

Question No:86 (Marks:1) **Vu-Topper RM**
Which of the following cannot be pushed on the stack?

- A. 1 byte**
- B. 2 byte
- C. 3 byte
- D. 4 byte

Question No:87 (Marks:1) **Vu-Topper RM**
Call instruction changes the values of _____ and _____ registers.

- A. IP,SP**
- B. IP,DX
- C. IP,AX
- D. IP,AL

Question No:88 (Marks:1) **Vu-Topper RM**
Which combination will you prefer to obtain the physical address of the stack?

- A. SP:SI
- B. SS:SI
- C. SP:DI
- D. SS:SP**

Question No:89 (Marks:1) **Vu-Topper RM**
Which of the following instructions is used for non-destructive AND operation?

- A. CMP
- B. ADC
- C. TEST**

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D. NAND

Question No:90

(Marks:1)

Vu-Topper RM

When the control is transferred to a subroutine?

A. When the return address is recovered

B. After the arguments are pushed on the stack

C. When the instruction pointer is not available

D. After the arguments are popped out from the stack

Question No:91

(Marks:1)

Vu-Topper RM

What is the content of stack pointer?

A. Address of the next instruction

B. Address of the current instruction

C. Address of the top element of the stack **Page 60**

D. Address of the lower element of the stack

Question No:92

(Marks:1)

Vu-Topper RM

A 16-bit processor has an accumulator of _____.

A. 16 Bit **Page 12**

B. 8 Bit

C. 32 Bit

D. 64 Bit

Question No:93

(Marks:1)

Vu-Topper RM

The maximum amount of memory accessible using 8085 processor is

_____.

A. 64 KB **Page 15**

B. 46 MB

C. 66 KB

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D. 64 BK

Question No:94

(Marks:1)

Vu-Topper RM

VGA stands for _____.

A. Video Graphics Adapter **Page 71**

B. Visual Graphics Adapter

C. Video Graphics Application

D. Video Graphic Accumulator

Question No:95

(Marks:1)

Vu-Topper RM

The _____ flag has a special role in debugging.

A. Sign

B. Zero

C. Trap **Page 17**

D. Parity

Question No:96

(Marks:1)

Vu-Topper RM

Which of the following is the general form of addressing?

A. Base + Index

B. Index + Offset

C. Base + Index + Offset **Page 35**

D. Base + Effective Address

Question No:97

(Marks:1)

Vu-Topper RM

All mathematical and logical operations are performed on the

_____.

A. Flags Register

B. Accumulator

C. Program Counter

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D. Arithmetic Logic Unit

Google

Question No:98

(Marks:1)

Vu-Topper RM

Which of the following register is used to hold address of the next instruction to be executed?

- A. Index register
- B. Counter register

C. Program Counter Page 13

D. Accumulator register

Question No:99

(Marks:1)

Vu-Topper RM

REPE and REPNE prefixes are only meaningful with _____.

- A. MOV
- B. LODS
- C. STOS

D. CMPS Page 85

Question No:100

(Marks:1)

Vu-Topper RM

Which of following is an illegal assembly instruction?

- A. mov al, bl
- B. mov ax, bx
- C. mov [num1], al

D. mov [num1], [num2] Page 30

Question No:101

(Marks:1)

Vu-Topper RM

sub sp, 2

The above instruction is used to perform a _____ operation on stack.

- A. RET

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B. POP

C. PUSH Page76

D. RET N

Question No:102

(Marks:1)

Vu-Topper RM

In direct addressing, the memory address given in the instruction is

_____.

A. fixed Page 28

B. Empty

C. Register

D. Variable

Question No:103

(Marks:1)

Vu-Topper RM

Which of the following are the basic bitwise logical operations?

A. AND, NOR, OR and NOT

B. NOT, OR, AND, and NNOT

C. AND, OR, XOR, and NOT Page 58

D. AND, NOR, OR and NOT

Question No:104

(Marks:1)

Vu-Topper RM

When the relative address stored with the instruction is in 16-bits, the jump is called a _____ jump.

A. Far

B. Near Page 45

C. Long

D. Short

Question No:105

(Marks:1)

Vu-Topper RM

Registers are storage cells _____.

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A. inside the memory

B. inside the processor Page 11

C. outside the processor

D. both inside and outside the processor

Question No:106

(Marks:1)

Vu-Topper RM

_____ subtracts one from the operand.

A. MCE

B. Shift

C. DEC Page 47

D. Modulus

Question No:107

(Marks:1)

Vu-Topper RM

STI stands for?

A. Set Interrupt

B. Set Instruction

C. Set the Interrupt flag

D. Standard Timer Interrupt

Question No:108

(Marks:1)

Vu-Topper RM

A symbol associated to an instruction in the program is known as:

A. Stack

B. Label Page 17

C. Jump

D. Subroutines

Question No:109

(Marks:1)

Vu-Topper RM

CX register is mostly used as a _____ register.

A. Flag

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B. Base

C. Counter Page 32

D. Destination

Question No:110

(Marks:1)

Vu-Topper RM

Whenever an instruction needs a memory source, _____ holds the pointer to it.

A. ES:DI

B. DS:SI Page 83

C. EX:DX

D. DS:SP

Question No:111

(Marks:1)

Vu-Topper RM

Keeping in view the downward compatibility between the two systems, the codes written for the Intel 8088 are _____ on the Intel 386 processor.

A. Valid Page 14

B. Altered

C. Invalid

D. Control

Question No:112

(Marks:1)

Vu-Topper RM

In multiplication algorithm, we take the first digit of the multiplier and multiply it with the _____.

A. Result

B. Divider

C. Reminder

D. Multiplicand Page 43

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Question No:113

(Marks:1)

Vu-Topper RM

The ordering of a program's instructions is ensured by the _____ register.

- A. Flags
- B. Index
- C. Stack Pointer

D. Program Counter Page 13

Question No:114

(Marks:1)

Vu-Topper RM

When an element is pushed on the stack, SP is decremented by _____.

- A. 1
- B. 2**
- C. 3
- D. 4

Page 60

Question No:115

(Marks:1)

Vu-Topper RM

In _____ operation, a zero is inserted from the left and every bit moves one position to the right. The right most bit is dropped into the carry flag.

- A. Shift logical Left (SHL)
- B. Shift Logical Right (SHR)**
- C. Shift Arithmetic Left (SAL)
- D. Shift Arithmetic Right (SAR)

Page 52

Question No:116

(Marks:1)

Vu-Topper RM

Which of the following flags sets when a larger number is subtracted from a smaller number?

- A. SP

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B. CF **Page 88**

C. ZF

D. OF

Question No:117

(Marks:1)

Vu-Topper RM

Which of the following bit is dropped into the carry flag after the execution of “Shift Logical Right” operation?

A. 0 bit

B. 1 bit

C. Left most bit

D. Right most bit **Page 52**

Question No:118

(Marks:1)

Vu-Topper RM

In ADC instruction, there are _____.

A. two registers only

B. two operands and the ZF

C. two registers and one memory location

D. Two operands and the CF **Page 57**

Question No:119

(Marks:1)

Vu-Topper RM

The most convenient place to store local variables is _____.

A. Stack **Page 75**

B. Registers

C. Main memory

D. Cache memory

Question No:120

(Marks:1)

Vu-Topper RM

The segment:offset pair is called a/an _____ address.

A. Virtual

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B. Logical **Page 22**

- C. Physical
- D. Effective

Question No:121 **(Marks:1)** **Vu-Topper RM**

The extra bit produced as a result of an arithmetic operation that does not fit in the target register is stored in:

A. Zero Flag

B. Carry flag **Page 16**

- C. Overflow Flag
- D. Interrupt Flag

Question No:122 **(Marks:1)** **Vu-Topper RM**

Cell width refers to the total number of bits in a memory cell while the total number of cells is called the _____.

A. Depth **Page 10**

- B. Cell width
- C. Cell Height
- D. Cell Length

Question No:123 **(Marks:1)** **Vu-Topper RM**

Intel 8085 can access up to _____ of memory, whereas Intel 8088 can access up to _____ of memory.

A. 64 KB, 1 MB **Page 15**

- B. 1 MB, 64 KB
- C. 64 KB, 1 GB
- D. 16 bits, 32 bits

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Question No:124

(Marks:1)

Vu-Topper RM

The base pointer accesses local variables using _____ offsets.

- A. Fixed
- B. Positive
- C. Negative**
- D. Incremental

Page 67

Question No:125

(Marks:1)

Vu-Topper RM

ASCII table is the contiguous arrangement of the uppercase alphabets (41-5A), the lowercase alphabets(61-7A), and the numbers

- A. 31-40
- B. 29-39
- C. 30-39**
- D. 31-41

Page 80

Question No:126

(Marks:1)

Vu-Topper RM

The correct instruction to subtract with borrow is _____.

- A. SB
- B. SBB**
- C. SWB
- D. SUBB

Page 49

Question No:127

(Marks:1)

Vu-Topper RM

mov [1234], ax is an example of _____ addressing.

- A. Direct**
- B. base + index
- C. register indirect
- D. base register indirect

Page 27

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Question No:128

(Marks:1)

Vu-Topper RM

Which of the following bus is used to inform the memory that whether processor wants to read data or write data?

A. Data Bus

B. Control Bus **Page 9**

C. Function Bus

D. Address Bus

Question No:129

(Marks:1)

Vu-Topper RM

In far jump, _____.

A. only offset is given

B. only segment address is given

C. both offset and segment are given **Page 46**

D. no need to mention any segment or offset

Question No:130

(Marks:1)

Vu-Topper RM

In which of the following addressing, both registers are not constant?

A. Base + Index **Page 36**

B. Index + offset

C. Base + offset

D. Base + offset + Index

Question No:131

(Marks:1)

Vu-Topper RM

The number of _____ in a cell is called the cell width.

A. Bits **Page 2**

B. Word

C. Bytes

D. Nibbles

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Question No:132

(Marks:1)

Vu-Topper RM

In extended multiplication, we store the multiplicand in _____ bits, and the result is stored in _____ bits.

A. 32,64

B. 32, 32 Page 57

C. 64,64

D. 16,32

Question No:133

(Marks:1)

Vu-Topper RM

Synchronization between the processor and the memory for read and write operations is done by the _____.

A. Data bus

B. Cache bus

C. Control bus Page 11

D. Address bus

Question No:134

(Marks:1)

Vu-Topper RM

Shift logical Right (SHR) moves all the bits one position to the _____, and inserts a zero from the _____.

A. left, left

B. left, right Page 52

C. Right, left

D. right, right

Question No:135

(Marks:1)

Vu-Topper RM

In MUL instruction, if the source operand is a byte, then it is multiplied with _____ register.

A. BL

B. AL Page 79

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- C. BH
- D. AH

Question No:136 (Marks:1) **Vu-Topper RM**
_____ transfers the word at the top of the stack (pointed to by SP) to the destination operand, and increments SP by two.

- A. Call
- B. POP Page 63**
- C. Push
- D. Ret n

Question No:137 (Marks:1) **Vu-Topper RM**
Which of the following jumps is taken if the last arithmetic operation did not change the sign unexpectedly?

- A. JO
- B. JNO Page 43**
- C. JE and JZ
- D. JNE and JNZ

Question No:138 (Marks:1) **Vu-Topper RM**
_____ or _____ are taken if the last arithmetic operation has produced a number in its destination that has odd parity.

- A. JP, JPE
- B. JS, JNS
- C. JNP, JPO Page 43**
- D. JCX, JNS

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Question No:139

(Marks:1)

Vu-Topper RM

Group of bits processor uses to inform memory which element to read/write is collectively known as:

- A. RAM
- B. Data bus
- C. Control bus

D. Address bus **Page 9**

Question No:140

(Marks:1)

Vu-Topper RM

MOVS is used to _____ a block of memory.

- A. Push
- B. Move**
- C. Save
- D. Delete

Page 84

Question No:141

(Marks:1)

Vu-Topper RM

_____ instruction directs the flow of program.

- A. Move
- B. Special
- C. Arithmetic

D. Program Control **Page 14**

Question No:142

(Marks:1)

Vu-Topper RM

Which of the following instructions allows memory to memory movement of data?

- A. MOV
- B. LODS
- C. MOVS**
- D. SCAS

Page 91

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Question No:143

(Marks:1)

Vu-Topper RM

By default, IP is associated with _____.

A. CS Page 34

B. BP

C. CX

D. SS

Question No:144

(Marks:1)

Vu-Topper RM

After the execution of Shift Arithmetic Right instruction, the most significant bit _____.

A. is replaced by a 0

B. is replaced by a 1

C. Retains its original value Page 52

D. is replaced by the value of carry flag

Question No:145

(Marks:1)

Vu-Topper RM

Which part of B80500 (encoded instruction) is an opcode?

A. B8 Page 25

B. B80

C. 0500

D. B80500

Question No:146

(Marks:1)

Vu-Topper RM

Which of the following jump is taken if the last arithmetic operation has produced a number in its destination that has even parity?

A. JNP

B. JPE Page 43

C. JNS

D. JPO

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Question No:147

(Marks:1)

Vu-Topper RM

During program execution, if any change occurs in AH or AL, is also reflected in _____.

A. AX Page 15

B. DX

C. AL

D. AH

Question No:148

(Marks:1)

Vu-Topper RM

In _____, a zero is inserted from the right and every bit moves one position to its left with the most significant bit dropped into the carry flag.

A. SHL

B. SAR

C. Both

D. SAL Page 44

Question No:149

(Marks:1)

Vu-Topper RM

Conditional jumps can only be _____.

A. Far

B. Near

C. Long

D. Short Page 46

Question No:150

(Marks:1)

Vu-Topper RM

BP stands for _____ pointer.

A. Bit

B. Byte

C. Base Page 16

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D. Basic

Question No:151

(Marks:1)

Vu-Topper RM

In video memory, each screen location corresponds to _____ bytes.

A. 1 Page 72

B. 2

C. 4

D. 8

Question No:152

(Marks:1)

Vu-Topper RM

Which of following registers is used as a counter?

A. AX

B. BX

C. CX Page 15

D. DX

Question No:153

(Marks:1)

Vu-Topper RM

Which of the following addressing scheme has been used in the instruction MOV [BX], AX?

A. Base + Index

B. Base + Offset

C. Base Register Direct

D. Based Register Indirect Page 35

Question No:154

(Marks:1)

Vu-Topper RM

The multiplier is stored in _____ bits in Extended Multiplication.

A. 8

B. 16 Page 49

C. 32

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D. 64

Question No:155

(Marks:1)

Vu-Topper RM

To modify the processor's behavior, _____ instructions are used.

A. Special Page 14

B. Arithmetic logic

C. Program control

D. Data movement

Question No:156

(Marks:1)

Vu-Topper RM

Which of the following uses data segment by default?

A. Base pointer

B. Base register Page 35

C. Stack pointer

D. Instruction pointer

Question No:157

(Marks:1)

Vu-Topper RM

In assembly language, the first executable instruction of the program should be placed at the offset _____.

A. 0x1100

B. 0x0100 Page 26

C. 0x0000

D. 0x0010

Question No:158

(Marks:1)

Vu-Topper RM

In the instruction "mov word [es:0], 0x1230", 12 refers to _____ color on _____ background.

A. black, green

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B. blue, green

C. green, blue

Page 81

D. green, black

Question No:159

(Marks:1)

Vu-Topper RM

XOR can also be used as a _____ to invert selective bits.

A. Source operand

B. Masking operator

Page 52

C. Destination operand

D. Multiplication operator

Question No:160

(Marks:1)

Vu-Topper RM

What does the given instruction do?

SHR DL, 1

A. Store the 1 bit in DL

B. Move the left most bit into the carry flag

C. Move the right most bit into the carry flag

Page 47

D. Shift left and store the most significant bit in the carry flag

Question No:161

(Marks:1)

Vu-Topper RM

Physical memory address is of:

A. 8 Bits

B. 16 Bits

C. 20 Bits

Page 21

D. 32 Bits

Question No:162

(Marks:1)

Vu-Topper RM

Which of the following is the correct syntax for 'OR' operation in assembly language?

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A. ax, or bx

B. or ax, bx Page 59

C. ax or bx

D. ax or, bx

Question No:163

(Marks:1)

Vu-Topper RM

In assembly language, "DIV BL" instruction has a/an _____ bit operand.

A. 8 Page 85

B. 16

C. 32

D. 64

Question No:164

(Marks:1)

Vu-Topper RM

Flags register is a special register in every architecture. It is also known as _____.

A. carry flag

B. special register

C. accumulator register

D. Program status word

Page 12

Question No:165

(Marks:1)

Vu-Topper RM

An important role of the stack is in the creation of _____ variables that are only needed while the subroutine is in execution and not afterwards.

A. Public

B. Local Page 67

C. Private

D. Global

بري صحبت سے تہائی بہتر ہے اور تہائی سے نیک صحبت بہتر ہے

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Question No:166

(Marks:1)

Vu-Topper RM

Stack is:

- A. a procedure
- B. an algorithm
- C. A data structure Page 59**
- D. both algorithm and data structure

Question No:167

(Marks:1)

Vu-Topper RM

Constant can never be used as _____.

- A. Source
- B. Destination Page 25**
- C. immediate source
- D. both source and destination

Question No:168

(Marks:1)

Vu-Topper RM

SP is associated (by default) with _____ register.

- A. ES
- B. SS Page 34**
- C. CS
- D. DS

Question No:169

(Marks:1)

Vu-Topper RM

The execution of the instruction "mov word [ES : 160], 0x1230" will print a character on the screen at the _____ column of the _____ row.

- A. first, third
- B. second, first
- C. first, second Page 81**
- D. second, second

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Question No:170

(Marks:1)

Vu-Topper RM

_____ can also be used as a masking operation to invert selective bits.

- A. OR
- B. NOT
- C. XOR Page 52**
- D. AND

Question No:171

(Marks:1)

Vu-Topper RM

The stack pointer marks the _____ of stack.

- A. Top Page 68**
- B. Middle
- C. Button
- D. Top and bottom

Question No:172

(Marks:1)

Vu-Topper RM

Far jump is not position relative rather _____.

- A. Indirect
- B. Absolute Page 46**
- C. Temporary
- D. Memory dependent

Question No:173

(Marks:1)

Vu-Topper RM

Which of the following are the two variants of STOS instruction?

- A. STOS1 and STOS2
- B. STOS and STOSES
- C. STOSA and STOSB
- D. STOSB and STOSW Page 86**

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Question No:174

(Marks:1)

Vu-Topper RM

Shift Arithmetic Right (SAR) shifts every bit one place to the right and places a copy of the _____ significant bit at the _____ significant place.

A. least, least

B. most, most

Page 52

C. least, most

D. most, least

Question No:175

(Marks:1)

Vu-Topper RM

There are _____ columns per row on the screen.

A. 60

B. 80 **Page 79**

C. 40

D. 20

Question No:176

(Marks:1)

Vu-Topper RM

Program consists of _____ logical parts.

A. One

B. Two **Page 20**

C. Four

D. Five

Question No:177

(Marks:1)

Vu-Topper RM

8088 is a _____ processor with its accumulator and all registers of _____.

A. 8 bit, 8 bits

B. 16 bit, 16 bits **Page 14**

C. 32 bit, 32 bits

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D. 16 bit, 32 bits

Question No:178

(Marks:1)

Vu-Topper RM

Which of the following keywords is used to define two bytes in memory?

A. DB

B. DW Page 25

C. DN

D. DD

Question No:179

(Marks:1)

Vu-Topper RM

_____ jump is absolute and not position relative.

A. Far Page 46

B. Near

C. Shot

D. Extended

Question No:180

(Marks:1)

Vu-Topper RM

Which of the following directive is used to reserve an eight bits space in memory?

A. db Page 25

B. dd

C. dq

D. dw

Question No:181

(Marks:1)

Vu-Topper RM

What makes db different from dw?

A. db occupies 1 bit while dw occupies 2 bits

B. db occupies 4 bits while dw occupies 8 bits

بري صحبت سے تنہائی بہتر ہے اور تنہائی سے نيك صحبت بہتر ہے

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C. dw occupies 8 bits and db occupies 16 bits

D. dw occupies 16 bits and db occupies 8 bits Page 25

Question No:182

(Marks:1)

Vu-Topper RM

_____ is used for permanent diversion.

- A. Ret
- B. Call
- C. Array

D. Jump

Page 63

Question No:183

(Marks:1)

Vu-Topper RM

The intent of the processor is carried to the memory for read/write operations through the _____.

- A. data bus
- B. address bus

C. Control bus Page 11

D. both data bus and address bus

Question No:184

(Marks:1)

Vu-Topper RM

How jmp is different from jnz?

- A. jmp permanently diverts the program flow, but jnz does not
- B. jnz permanently diverts the program flow, but jmp does not
- C. jmp is a conditional jump, however jnz is an unconditional jump

D. jmp is an unconditional jump, however jnz is a conditional jump

Question No:185

(Marks:1)

Vu-Topper RM

There is an auto-increment mode when the _____ is

_____.

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- A. DF, set
- B. ZF, not set
- C. DF, not set**
- D. CX, not set

Question No:186

(Marks:1)

Vu-Topper RM

Unconditional jump _____.

- A. Never transfers the control
- B. Always transfer the control**
- C. Transfers the control if the condition is true
- D. Transfers the control if the condition is false

Question No:187

(Marks:1)

Vu-Topper RM

num1: 75,50,77,82

Which of the following accesses the third element i.e. 77?

- A. num1[2]**
- B. [num1+4]
- C. [num1+3]
- D. [num1+2]

Question No:188

(Marks:1)

Vu-Topper RM

In assembly language programming, "JNZ" is used to jump if the _____ flag _____.

- A. sign, is set
- B. zero, is not set** **Google**
- C. sign, is not set
- D. Zero, is set

Question No:189

(Marks:1)

Vu-Topper RM

Which of the following is used to clear the direction flag?

- A. Std

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- B. Stdr
- C. Clrd
- D. Cld**

Question No:190

(Marks:1)

Vu-Topper RM

Which of the following has magnitude and sign?

- A. Octal number
- B. Signed number**
- C. Unsigned number
- D. Hexadecimal number

Question No:191

(Marks:1)

Vu-Topper RM

Push and pop operations always operate on_____.

- A. Byte
- B. Float
- C. Words**
- D. Integer

Page 80

Question No:192

(Marks:1)

Vu-Topper RM

Suppose AX=5, BX=5, DX=0, CF=1, ZF=1 and AF=1. What will be the final value in AX register after the execution of ADC AX, BX?

- A. 5
- B. 6
- C. 10
- D. 11**

Question No:193

(Marks:1)

Vu-Topper RM

Which of the following is the renamed version of conditional jump JZ?

A. JE

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- B. JA
- C. JB
- D. JNC

Question No:194 (Marks:1) **Vu-Topper RM**

If AX=00FF, then which of the following instruction can be used to change the value of AX to FF00?

- A. NOT AX
- B. OR AX, AX
- C. XOR AX, AX
- D. AND AX, FF00**

Question No:195 (Marks:1) **Vu-Topper RM**

Conditional jump can only be _____.

- A. Far
- B. Near
- C. Long

D. Short **Page 36**

Question No:196 (Marks:1) **Vu-Topper RM**

REPNEZ repeats the following instruction while the _____.

- A. ZF is set
- B. ZF is not set** **Page 85**
- C. Direction Flag is set
- D. Direction Flag is not set

Question No:197 (Marks:1) **Vu-Topper RM**

To declare a character in assembly language, we store its ASCII code in a _____.

بري صحبت سے تنہائی بہتر ہے اور تنہائی سے نيك صحبت بہتر ہے

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A. Float

B. Byte Page 74

C. Words

D. Integer

Question No:198 (Marks:1) **Vu-Topper RM**

REP with MOVS utilizes the _____ power of a processor to do scrolling in minimum time.

A. Full

B. Half

C. Small

D. Quarter

Question No:199 (Marks:1) **Vu-Topper RM**

The basic function of register is to hold _____.

A. Opcodes

B. Operators

C. Oparands

D. Mnemonics

Question No:200 (Marks:1) **Vu-Topper RM**

Stack is a data structure that behaves in _____ manner.

A. LILO

B. FILO

C. LIFO Page 59

D. Both LIFO and FIFO

Question No:201 (Marks:1) **Vu-Topper RM**

In _____ operation, the output is 1 only if both inputs are 1.

بري صحبت سے تنہائی بہتر ہے اور تنہائی سے نيك صحبت بہتر ہے

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- A. OR
- B. XOR
- C. AND**
- D. NOT

Goolge

Question No:202

(Marks:1)

Vu-Topper RM

Which of the following is the most illegal instruction?

- A. Mov ax, al
- B. Mov al, [num1]
- C. Mov al, ax**
- D. Mov ax, [num1]

Question No:203

(Marks:1)

Vu-Topper RM

Registers are normally part of _____.

- A. CPU**
- B. RAM
- C. Hard disk
- D. CPU and Cache

Question No:204

(Marks:1)

Vu-Topper RM

EBCDIC, grey code, and ASCII are the standards for _____ representation of characters.

- A. Generic
- B. Numeric
- C. Alphabetic
- D. Alpha-numeric**

برى صحبت سے تنهائى بهتر ہے اور تنهائى سے نيك صحبت بهتر ہے

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Question No:205

(Marks:1)

Vu-Topper RM

Which part of machine code tells the central processor to perform a certain task.

- A. SS
- B. Mov
- C. Operation code**
- D. CALL operation

Question No:206

(Marks:1)

Vu-Topper RM

Suppose, the current value of IP is 0x0129, and the relative address is 0x0012. What will be the new value of IP?

- A. 0x0135
- B. 0x0112
- C. 0x013B**
- D. 0x0129

Question No:207

(Marks:1)

Vu-Topper RM

mov ax, [num1] is an example of _____ bit move.

- A. 8**
- B. 16
- C. 24
- D. 32

Question No:208

(Marks:1)

Vu-Topper RM

The physical memory address of 1234:5678 segment-offset pair is:

- A. 179B8**
- B. 174B8
- C. 179F8
- D. 179C8

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Question No:209

(Marks:1)

Vu-Topper RM

_____ cannot appear as an operand in any instruction. It is used for accessing instructions.

- A. SS
- B. IP**
- C. BH
- D. AH

Question No:210

(Marks:1)

Vu-Topper RM

Which of the following is the extension of object file?

- A. .exe**
- B. .lst
- C. .com
- D. .asm

Question No:211

(Marks:1)

Vu-Topper RM

Shifting the integer 5 left by 1 bit results in _____.

- A. 5
- B. 10**
- C. 25
- D. 50

Question No:212

(Marks:1)

Vu-Topper RM

Memory address always goes from _____.

- A. memory to memory
- B. Processor to memory**
- C. processor to register
- D. memory to processor

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Question No:213

(Marks:1)

Vu-Topper RM

Simple CMP instruction uses _____ operation.

- A. Division
- B. Division
- C. Subtraction**
- D. Multiplication

Question No:214

(Marks:1)

Vu-Topper RM

The mechanism used to drop the carry for making the computed address valid is known as _____.

- A. Overflow
- B. carry overload
- C. address wraparound**
- D. segment overlapping

Question No:215

(Marks:1)

Vu-Topper RM

A/An _____ is an area of memory that holds all local variables and parameters.

- A. Stack**
- B. base register
- C. data segment
- D. instruction pointer

Question No:216

(Marks:1)

Vu-Topper RM

_____ is used for temporary diversion.

- A. Jump
- B. Call**
- C. Return
- D. Shot jump.

Question No:217

(Marks:1)

Vu-Topper RM

The swap flag can be stored in _____.

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A. Memory

B. A Register

Page 40

C. both register and memory

D. neither register nor memory

Question No:218

(Marks:1)

Vu-Topper RM

In the instruction "mov word [es:160], 0x1230", 30 represents the character _____.

A. 1

B. A

C. B

D. 0

Question No:219

(Marks:1)

Vu-Topper RM

Multiplying two 4 bit numbers result in a(an) _____ bit number.

A. 7

B. 8

C. 5

D. 6

Question No:220

(Marks:1)

Vu-Topper RM

In "Base + Offset" addressing, the value contained in the base register is added with the offset to get the _____ address.

A. Virtual

B. Linear

C. Physical

D. Effective

Question No:221

(Marks:1)

Vu-Topper RM

cli stands for?

A. clock interrupt

B. cross line instruction

C. control line instruction

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D. Clear the interrupt flag **Page 6**

Question No:222 **(Marks:1)** **Vu-Topper RM**

DW is used to reserve _____ bit value in memory.

- A. 8
- B. 16**
- C. 24
- D. 32

Question No:223 **(Marks:1)** **Vu-Topper RM**

90 is the op-code of

- A. Add
- B. Subtract
- C. Do nothing**
- D. Multiplication

Question No:224 **(Marks:1)** **Vu-Topper RM**

BH register is a (an) _____ bit register.

- A. 8** **Google**
- B. 16
- C. 24
- D. 32

Question No:225 **(Marks:1)** **Vu-Topper RM**

CS and IP are both _____ bit registers.

- A. 8
- B. 16** **Page 13**
- C. 32
- D. 64

Question No:226 **(Marks:1)** **Vu-Topper RM**

Which of the following is a Program Control Instruction?

- A. Cli

بري صحبت سے تنہائی بہتر ہے اور تنہائی سے نيك صحبت بہتر ہے

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B. jne 0534

C. mov ax, bx

D. add bx, 0534

Question No:227

(Marks:1)

Vu-Topper RM

REPE or REPNE are used with the _____ instruction.

A. Call

B. Add

C. SCAS

Page 85

D. MOVS

Question No:228

(Marks:1)

Vu-Topper RM

Sending the appropriate signals on the control bus to the memory is the responsibility of _____.

A. Memory

B. Data Bus

C. Processor

D. Control Bus

Question No:229

(Marks:1)

Vu-Topper RM

"mov [bx], ax" moves the two bytes contents of the AX register to the address contained in BX register in the current _____.

A. Extra Segment

B. Code Segment

C. Data Segment

D. Stack Segment

Question No:230

(Marks:1)

Vu-Topper RM

In which of the following addressing, the memory address is fixed and is given in the instruction?

A. Direct

B. Indexed

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- C. Indirect
- D. Base plus offset

Question No:231

(Marks:1)

Vu-Topper RM

There are _____ block processing instructions in 8088.

- A. 6
- B. 4
- C. 5**
- D. 3

Page 83

Question No:232

(Marks:1)

Vu-Topper RM

Whenever we need access to a memory location whose address is not known until run-time we use _____.

- A. Index register**
- B. Counter Register
- C. Destination Register
- D. Accumulator Register

Page 4

Question No:233

(Marks:1)

Vu-Topper RM

Our computer screen is like a 2-D array having _____ rows and _____ columns.

- A. 80, 25
- B. 25, 40**
- C. 40, 25
- D. 25, 80

Question No:234

(Marks:1)

Vu-Topper RM

REPE repeats a string instruction while the _____ flag is set.

- A. Carry
- B. Zero**
- C. Parity
- D. Direction

Page 32

بري صحبت سے تنہائی بہتر ہے اور تنہائی سے نیک صحبت بہتر ہے

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Question No:235

(Marks:1)

Vu-Topper RM

Each bit of the _____ register conveys a different meaning.

- A. Index
- B. Flags**
- C. Pointer
- D. Accumulator

Question No:236

(Marks:1)

Vu-Topper RM

When SI or DI are used, we name the method _____ addressing.

- A. Stack
- B. Based
- C. Indexed**
- D. Segment

Page 24

Question No:237

(Marks:1)

Vu-Topper RM

In general, width of a memory cell cannot be greater than the width of _____.

- A. I/O Bus
- B. Data bus.**
- C. Control Bus
- D. Address Bus

Question No:238

(Marks:1)

Vu-Topper RM

Code size reduction and improvement in speed were the two reasons for introducing block processing instructions in the _____ processor.

- A. 8088**
- B. 8085
- C. 8080
- D. iAPX386

بري صحبت سے تنہائی بہتر ہے اور تنہائی سے نيك صحبت بہتر ہے

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Question No:239

(Marks:1)

Vu-Topper RM

The first instruction of "COM" file must be at offset

- A. 0x0010
- B. 0x0100**
- C. 0x1000
- D. 0x0000

Question No:240

(Marks:1)

Vu-Topper RM

One screen location corresponds to a

- A. Byte
- B. Word**
- C. Double type
- D. Double word

Question No:241

(Marks:1)

Vu-Topper RM

When a 32 bit number is divided by a 16 bit number, the remainder is of

- A. 32 bits
- B. 16 bits**
- C. 8 bits
- D. 64

Question No:242

(Marks:1)

Vu-Topper RM

Which bit of the attribute byte represents the blue component of foreground color?

- A. 0** **Page 73**
- B. 1
- C. 2
- D. 3

بري صحبت سے تھائی بہتر ہے اور تھائی سے نیک صحبت بہتر ہے

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Question No:243

(Marks:1)

Vu-Topper RM

The total number of symbols in extended ASCII are _____.

A. 64

B. 128 Page 71

C. 156

D. 256

Question No:244

(Marks:1)

Vu-Topper RM

In case of short jump, the offset is stored in _____.

A. 1 byte Page 38

B. 2 bytes

C. 4 bytes

D. 16 bytes

Question No:245

(Marks:1)

Vu-Topper RM

Which of the following will be the result of left shifting 9C40 once?

A. 3881

B. 1388 Page 48

C. 3880

D. 3882

Question No:246

(Marks:1)

Vu-Topper RM

When there is a 16 bit operand with the DIV instruction, then the implied dividend will be in _____ bits.

A. 8

B. 16 Page 77

C. 32

D. 64

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Question No:247

(Marks:1)

Vu-Topper RM

Suppose, BX=0x0120, CS=0x1000, and the memory under consideration is [CS:BX+0x0880].

A. 109A0

B. 10834

C. 11A00 Google

D. 11100

Question No:248

(Marks:1)

Vu-Topper RM

There are _____ forms of the DIV instruction.

A. One

B. Two Page 77

C. Five

D. Three

Question No:249

(Marks:1)

Vu-Topper RM

What makes CMP different from SUB?

A. Both CMP and SUB are destructive subtraction

B. SUB is a non destructive subtraction while CMP is a destructive one

C. SUB is a destructive subtraction while CMP is a non-destructive subtraction. Both affect the flags

D. CMP is a non destructive subtraction that does not affect any flag, however SUB is a destructive subtraction

Question No:250

(Marks:1)

Vu-Topper RM

This jump is taken if the last arithmetic operation has produced a positive number in its destination.

A. JO

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B. JP

C. JNS **Page 35**

D. JPE

Question No:251

(Marks:1)

Vu-Topper RM

The maximum memory accessed by Intel 8088 processor is

A. 1Mb **Page 12**

B. 2GB

C. 8 MB

D. 64 KB

Question No:252

(Marks:1)

Vu-Topper RM

In iAPX88, _____ flag is specially related to the string instructions.

A. Sign

B. Carry

C. Parity

D. Direction

Question No:253

(Marks:1)

Vu-Topper RM

In case of 32-bit processor, the size of an accumulator register will be _____ bits.

A. 8

B. 64

C. 16

D. 32

Question No:254

(Marks:1)

Vu-Topper RM

How logical errors are different from syntax errors?

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A. Syntax errors are more important than the logical errors

B. Identifying both types of errors is the responsibility of assembler

C. Identifying syntax errors is the responsibility of programmer unlike logical errors

D. Identifying syntax and logical errors is the responsibility of assembler and programmer respectively

Question No:255

(Marks:1)

Vu-Topper RM

In shift left operation, the left most bit will _____.

A. always be 1

B. be discarded

C. go into the Carry Flag **Page 48**

D. be moved to the right most position

Question No:256

(Marks:1)

Vu-Topper RM

Which of the following is a special instruction?

A. Cli

B. cmp ax, 0

C. mov ax, bx

D. add bx, 0534

Question No:257

(Marks:1)

Vu-Topper RM

iAPX88 Architecture consists of

A. 32 Registers

B. 14 Registers **Page 7**

C. 10 Registers

D. 16 Registers

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Question No:258

(Marks:1)

Vu-Topper RM

The decimal representation of $0x17$ is _____.

A. 17

B. 23 **Google**

C. 32

D. 21

Question No:259

(Marks:1)

Vu-Topper RM

Which of the following jumps makes intra segment control possible?

A. Far **Page 38**

B. Long

C. Short

D. Jamp

Question No:260

(Marks:1)

Vu-Topper RM

REP allows an instruction to be _____ CX times.

A. Reported

B. Repeated **Page 90**

C. Replaced

D. Repositioned

Question No:261

(Marks:1)

Vu-Topper RM

Which of the following formulae calculates the desired location on the screen?

A. Location = (rowno * 80 + column) * 2

B. location = (rowno * 80 + columnno) * 8

C. location = (colmunno * 80 + rowno) * 2,

D. location = (rowno * 80 + columnno) * 6,

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Question No:262

(Marks:1)

Vu-Topper RM

What is the purpose of the following two instructions?

MOV AX, 0XB800
MOV ES, AX

- A. Pointing ES to audio base
- B. Pointing ES to video base
- C. Pointing ES to numeric base
- D. Pointing ES to graphics base**

Question No:263

(Marks:1)

Vu-Topper RM

There are ____ forms of MOVS instruction.

- A. One
- B. Two** **Page 90**
- C. Three
- D. Four

Question No:264

(Marks:1)

Vu-Topper RM

SCAS compares a source in accumulator register with the _____ string element addressed by ES:DI and updates the flags.

- A. Flags
- B. Source
- C. Register
- D. Destination** **Page 84**

Question No:265

(Marks:1)

Vu-Topper RM

In assembly language, integer division results in _____ quotient and _____ remainder.

- A. integer, integer**
- B. floating point, integer
- C. integer, floating point

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D. floating point, floating point

Question No:266

(Marks:1)

Vu-Topper RM

Which of the following directive used to reserve a 8 bit space in the memory holding data?

A. Db

B. dw

C. dd

D. dq

Question No:267

(Marks:1)

Vu-Topper RM

The 8088 processor divides interrupts into _____ classes.

A. One

B. Two

C. Three

D. Four

Question No:268

(Marks:1)

Vu-Topper RM

_____ register holds the address of next instruction is to be executed

A. Base pointer

B. Code segment

C. Source index

D. Program counter

Question No:269

(Marks:1)

Vu-Topper RM

After the execution of REP instruction CX will be decremented then which of the following flags will be affected?

A. CF

B. OF

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C. DF

D. No flags will be affected

Question No:270 (Marks:1)

Vu-Topper RM

JC and JNC test the _____ flag.

A. Carry

B. Parity

C. Zero

D. Sign

Question No:271 (Marks:1)

Vu-Topper RM

To convert any digit to its ASCII representation

A. Add 0x30 in the digit

B. Subtract 0x30 from the digit

C. Add 0x61 in the digit

D. Subtract 0x61 from the digit

Question No:272 (Marks:1)

Vu-Topper RM

The bits of the _____ work independently and individually.

A. Index register

B. Base register

C. Flags register

D. Accumulator

Question No:273 (Marks:1)

Vu-Topper RM

When an item is pushed on the decrementing stack, the top of the stack is

A. First decremented and then element copied to the stack

B. First incremented and then element copied to the stack

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- C. decremented after the element copied to the stack
- D. incremented after the element copied to the stack

Question No:274 (Marks:1) **Vu-Topper RM**

Each screen location corresponds to a word, the lower byte of this word contains _____.

- A. The character code**
- B. The attribute byte
- C. the parameters
- D. The dimensions

Question No:275 (Marks:1) **Vu-Topper RM**

If ax contains decimal -2 and BX contains decimal 2 then after the execution of

Instruction: CMP AX, BX, JA label

- A. Jump will be taken
- B. Zero flag will set
- C. 2F will contain value -4
- D. Jump will not be taken**

Question No:276 (Marks:1) **Vu-Topper RM**

Only instructions allow moving data from memory to memory.

- A. String**
- B. Word
- C. Indirect
- D. Stack

Question No:277 (Marks:1) **Vu-Topper RM**

mov ax,5 has

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A. 1 operand

B. 2 operand

C. 3 operand

Question No:278

(Marks:1)

Vu-Topper RM

The physical address of the stack is obtained by

A. SS:SI combination

B. SS:SP combination

C. ES:BP combination

D. ES:SP combination

Question No:279

(Marks:1)

Vu-Topper RM

Index registers are used to store _____

A. Data

B. Intermediate result

C. Address

D. Both data and addresses

Question No:280

(Marks:1)

Vu-Topper RM

If the direction of the processing of a string is from higher addresses towards lower addresses, then

A. ZF is cleared

B. DF is cleared

C. ZF is set

Question No:281

(Marks:1)

Vu-Topper RM

In STOS instruction, the implied source will always be in

A. AL or AX registers

B. DL or DX registers

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- C. BL or BX registers
- D. CL or CX registers

Question No:282

(Marks:1)

Vu-Topper RM

When a 32 bit number is divided by a 16 bit number, the quotient will be store in

- A. AX**
- B. BX
- C. CX
- D. DX

Question No:283

(Marks:1)

Vu-Topper RM

“mov byte [num1], 5” is _____ instruction.

- A. Legal**
- B. Illegal
- C. Stack bases
- D. Memory indirect

Question No:284

(Marks:1)

Vu-Topper RM

To transfer control back the RET instruction take

- A. 1 argument
- B. 2 arguments
- C. 3 arguments**
- D. No arguments

Question No:285

(Marks:1)

Vu-Topper RM

The basic function of SCAS instruction is to

- A. Compare**
- B. Scan

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- C. Sort
- D. Move data

Question No:286 (Marks:1) **Vu-Topper RM**
_____ is a special prefix that is used to repeat the block instructions. _____ is a special prefix that is used to repeat the block instructions.

- A. RPT
- B. REP**
- C. REPB
- D. REPEAT

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Question No:287 (Marks:1) **Vu-Topper RM**
Shift logical Right (SHR) operation copies the _____ bit into the carry flag.

- A. 0
- B. Left most
- C. Right most**
- D. Most significant

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Question No:288 (Marks:1) **Vu-Topper RM**
There are _____ registers in iAPX88 architecture that can hold address of data.

- A. 1
- B. 2
- C. 3

D. 4 **Page 22**

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Question No:289

(Marks:1)

Vu-Topper RM

In which of the following forms, a character is stored in memory while writing assembly language programs?

- A. Scan code
- B. Parity code
- C. Octal code

D. ASCII code

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Question No:290

(Marks:1)

Vu-Topper RM

A value 0500 is stored in memory. If we transfer this value to a general-purpose register, then it will be shown as:

- A. 0050
- B. 0005
- C. 0500
- D. 5000

Page 28

Question No:291

(Marks:1)

Vu-Topper RM

Which of following conditional jump is not dependant on any flag?

- A. JP
- B. JPE
- C. RCL

D. JCXZ

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Question No:292

(Marks:1)

Vu-Topper RM

In Shift logical Left (SHL), the _____ bit is dropped into the carry flag.

- A. Least significant
- B. Most significant
- C. Third least significant

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D. Second least significant

Question No:293

(Marks:1)

Vu-Topper RM

In STOS, the implied source operand always resides in

_____.

A. BL or BX

B. CL or CX

C. AL or AX Page 92

D. DL or DX

Question No:294

(Marks:1)

Vu-Topper RM

IAPX88 stands for _____.

A. Intel Active Processor Extensions 88

B. Intel Advanced Processor External 88

C. Intel Advanced Process Extensions 88

D. Intel Advanced Processor Extensions 88 Page 14

Question No:295

(Marks:1)

Vu-Topper RM

In _____, every bit moves one position to the right, and the bit dropped from the right is inserted at the left. The dropped bit is also copied into the carry flag.

A. Rotate Left (ROL)

B. Rotate Right (ROR) Page 53

C. Rotate through Carry Left (RCL)

D. Rotate through Carry Right (RCR)

Question No:296

(Marks:1)

Vu-Topper RM

B80500, B8 was the opcode and

A. 0500

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- B. 5000
- C. 0055
- D. 0505

Question No:297

(Marks:1)

Vu-Topper RM

This precise synchronization between the processor and the memory is the responsibility of the

- A. Control bus**
- B. System bus
- C. Logical bus
- D. Digital bus

Question No:298

(Marks:1)

Vu-Topper RM

8051 by the same manufacturer has an_____ stack

- A. Incrementing**
- B. Decrementing
- C. Both
- D. None of these

Question No:299

(Marks:1)

Vu-Topper RM

Mov [1234] ax is an example of — addressing.

- A. Direct**
- B. Undirect
- C. Pointer
- D. Physical

Question No:300

(Marks:1)

Vu-Topper RM

Which bit is attribute but representing the blue component of foreground color

- A. 0** **Page 73**
- B. 1
- C. 2
- D. 3

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Question No:301

(Marks:1)

Vu-Topper RM

The iAP888 architecture consists of _____ register.

A. 12

B. 14 **Page 15**

C. 16

D. 10

Question No:302

(Marks:1)

Vu-Topper RM

In MUL instruction, if the source operand is a word, then it is multiplied with _____ register.

A. AX **Page 87**

B. AH

C. AL

D. BL

Question No:303

(Marks:1)

Vu-Topper RM

When the operand of DIV instruction is in 16 bits, then the implied dividend will be in _____.

A. AX

B. EX:AX

C. DX:AX **Page 85**

D. DX:BX

Question No:304

(Marks:1)

Vu-Topper RM

In _____ operation, the Most Significant Bit (MSB) is inserted from the right, causing every bit to move one position to the left. The MSB is also copied into the carry flag.

A. ROR **Page 45**

B. RCL

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- C. CLR
- D. LOR

Question No:305

(Marks:1)

Vu-Topper RM

Which of the following is also called intro-segment call?

- A. Far call
- B. Near call**
- C. Short call
- D. Long call

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Question No:306

(Marks:1)

Vu-Topper RM

Which bit of the attribute byte represents the Blinking of foreground character?

- A. 7**
- B. 1
- C. 3
- D. 5

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Question No:307

(Marks:1)

Vu-Topper RM

Which bit of the attribute byte represents the Red component of background color?

- A. 2
- B. 4
- C. 8
- D. 6**

Page 7

Question No:308

(Marks:1)

Vu-Topper RM

Which bit of the attribute byte represents the Green component of background color?

- A. 3
- B. 5**
- C. 9

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D. 11

Question No:309

(Marks:1)

Vu-Topper RM

Which bit of the attribute byte represents the Blue component of background color?

A. 4 **Page 73**

B. 6

C. 8

D. 10

Question No:310

(Marks:1)

Vu-Topper RM

Which bit of the attribute byte represents the Intensity component of foreground color?

A. 3 **Page 73**

B. 4

C. 5

D. 6

Question No:311

(Marks:1)

Vu-Topper RM

"mov [bp], al" moves the one byte contents of the AL register to the address contained in the BP register in the current _____.

A. Data segment.

B. Stack segment.

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C. Code segment.

D. Extra segment.

Question No:312

(Marks:1)

Vu-Topper RM

Which of the following is an illegal instruction?

A. Mov BX, 10

B. MOV AX, BX **Page 22**

C. MOV AX, 65

D. MOV AX, [BX+BP]

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Question No:313

(Marks:1)

Vu-Topper RM

There are _____ types of address wraparound.

A. 2 **Page 29**

B. 4

C. 6

D. 8

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