

# Professional Online Academy Vu Topper RM



**CS302-Digital Logical Design**  
**Update Important Subjective**  
**Mid Term by Vu Topper RM**



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**PROFESSIONAL ONLINE ACADEMY**



**WE Offers**

LMS Handling

Important Notes

Online Classes

Assignments

Quiz & GDB's

Projects

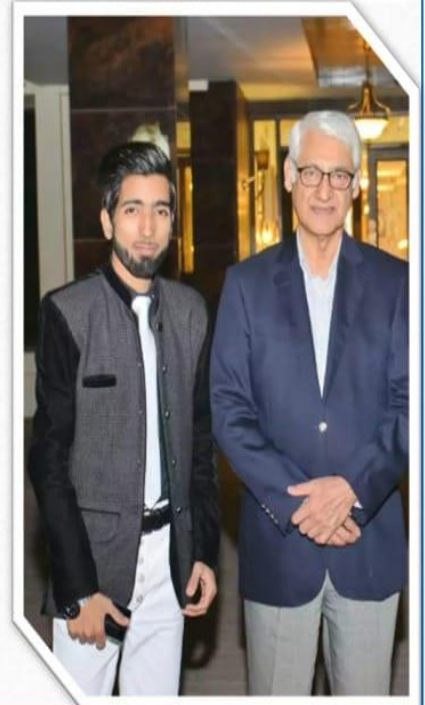
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## Very Important Topics with Question.

- 1)BCD.
- 2)Gray Code.
- 3)Comparator.
- 4)Multiplexer.
- 5)ABEL Topics.
- 6)PLA and PAL.
- 7)Logic diagram.
- 8)S-R NOR latch.
- 9)Frictional binary.
- 10) Sop and pos topics.
- 11) Comparative circuit.
- 12) Boolean expressions.
- 13) Bit comparator  $A > B = 1$ .
- 14) Uses of Demultiplexer?
- 15) Caveman number base 5.
- 16) SOP to POS conversation.
- 17) What is meant by ABEL?
- 18) Timing diagram of NOR gate.
- 19) Binary to Octal conversation.
- 20) K-map in convert of POS expression.

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- 21) Karnaugh map solve Boolean expression.
- 22) Difference b/w BCD to decimal Decoder.
- 23) Boolean expression of SOP and truth table.
- 24) The NAND gate-based S-R (Set-Reset) Latch.
- 25) Difference between BCD to decimal Decoder.
- 26) How many inputs and output bit do a half adder contain?
- 27) -Combinational circuit of given expression.  
 $(A+B).(A'+B').C.$
- 28) Convert 74 and 257 into BCD and put odd parity in their results.
- 29) Multiplexer convert 4-1-bit Boolean expression  
 $A.B.C = A'B + AC + B'C.$

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